

JASON POOVEY

EDUCATION

Georgia Institute of Technology, Atlanta, GA (Expected 2015)
Ph.D. in Computer Science (In Progress) - GPA: 4.0

North Carolina State University, Raleigh, NC (2009)
M.S. in Computer Engineering - GPA: 4.0

North Carolina State University, Raleigh, NC (2007)
B.S. in Computer Engineering; B.S. in Electrical Engineering – GPA: 4.0

TEACHING EXPERIENCE

Emory University (2014)
Adjunct Lecturer – “CS170: Introduction to Computer Science”
Lead instructor of freshman level Computer Science course based in Java

Georgia Institute of Technology (2012-2013)
Teaching Assistant – “CS6290: High Performance Computer Architecture”
Substitute Lecturer – CS6290/CS4290/ECE4100/ECE6100: High Performance Computer Architecture
Graduate Level course in Computer Architecture

North Carolina State University (2006-2009)
Adjunct Lecturer – “ECE 109: Introduction to Computing Systems”
Teaching Assistant – “ECE 109: Introduction to Computing Systems”
Freshman level course in Computer Architecture. Covers material from binary, logic gates, transistors, and basic assembly.

TEACHING EXPERTISE

Computer Architecture (beginning to doctoral level), Introductory programming (Assembly, C, C++, Java, any high level language), Advanced Programming (C, C++, Python), Simulation and Modeling, Compiler Design/Optimizations

RELATED EXPERIENCE

Georgia Tech Research Institute (2012-present)
Research Scientist
Researcher in the Cyber Technology and Information Security Lab in the Innovative Computing Division. Focus has been on research in big data analytics, parallel computing, and application specific novel architecture design.

Athenahealth (2012)
Software Developer
Developer on the Advanced Reporting team writing Perl code and SQL queries to enhance the

reporting infrastructure within AthenaNet

Georgia Institute of Technology (2009-2012)

Research Assistant

Research focus was on Parallel Pattern Detection for architectural improvements

Intel Corporation, Santa Clara, CA (2008)

Graduate Summer Intern

Developed an assembler from scratch for the Ct project; Defined the assembly language and interfaced with existing tools for Ct/Larabee

North Carolina State University, Raleigh, NC (2006-2009)

Research Assistant

Worked on a project to characterize an embedded benchmark suite (EEMBC); Worked in a team of graduate students assisting with publications and research

PUBLIC GRANTS/CONTRACTS

- **NEXTCACHE**, DARPA-BAA-12-64, November 27, 2013. \$346,547

SKILLS AND EXPERTISE

C, C++, Parallel Programming (OpenMP, pthread, MPI), Python, Perl, Javascript, NodeJS, AngularJS, Java, SQL, MongoDB, Flex, Bison

AWARDS

Engineering Dean's Scholarship

Dean's Fellowship

GSA University Wide Outstanding Teaching Assistant

PUBLICATIONS AND PAPERS

- [1] D. Appling, E. Briscoe, D. Ediger, J. Poovey, R. McColl "Deriving Disaster-Related Information from Social Media." KDD-LESI 2014
- [2] D. Ediger, D. Appling, E. Briscoe, R. McColl, J. Poovey "Real-Time Streaming Intelligence: Integrating Graph and NLP Analytics." HPEC, 2014
- [3] R. McColl, D. Ediger, J. Poovey, D. Campbell, and D.A. Bader "A Brief Study of Open Source Graph Databases," *ArXiv e-prints*. cs.DB 1309.2675. September, 2013.
- [4] J. G. Beu, J.A. Poovey, E.R. Hein, T.M. Conte, "High-Speed Formal Verification of Heterogeneous Coherence Hierarchies," HPCA 2013.
- [5] P. D. Bryan, J. A. Poovey, J. G. Beu and T. M. Conte, "Accelerating Multi-threaded Application Simulation Through Barrier-Interval Time- Parallelism," *Proceedings of the IEEE 20th International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS'12)*, (Washington, D.C), Aug., 2012.
- [6] R. A. Bheda, J. A. Poovey, J. G. Beu and T.M. Conte, "Energy Efficient Phase Change Memory Based Main Memory for Future High Performance Systems," *Proceedings of the 2nd International Green Computing Conference (IGCC'11)*, Orlando, Florida, July 25-28, 2011.

- [7] J. A. Poovey, B. P. Railing and T. M. Conte, "Parallel pattern detection for architectural improvements," *Proceedings of the 3rd USENIX Workshop on Hot Topics in Parallelism (HotPar)*, (Berkeley, CA), May 26–27, 2011.
- [8] Jason A. Poovey, Thomas M. Conte, Markus Levy, Shay Gal-On, "A Benchmark Characterization of the EEMBC Benchmark Suite," *IEEE Micro*, pp. 18-29, September/October, 2009
- [9] Balaji V. Iyer, Jason A. Poovey and Thomas M. Conte, "Energy-Aware Opcode Design," *23rd International Conference on Computer Design*, Lake Tahoe, CA, 2008
- [10] Jason A. Poovey, "Characterization of the EEMBC Benchmark Suite," *Technical Report, EEMBC, 2007*. Published on EEMBC website at <http://www.eembc.org/benchmark/characterization.pdf>